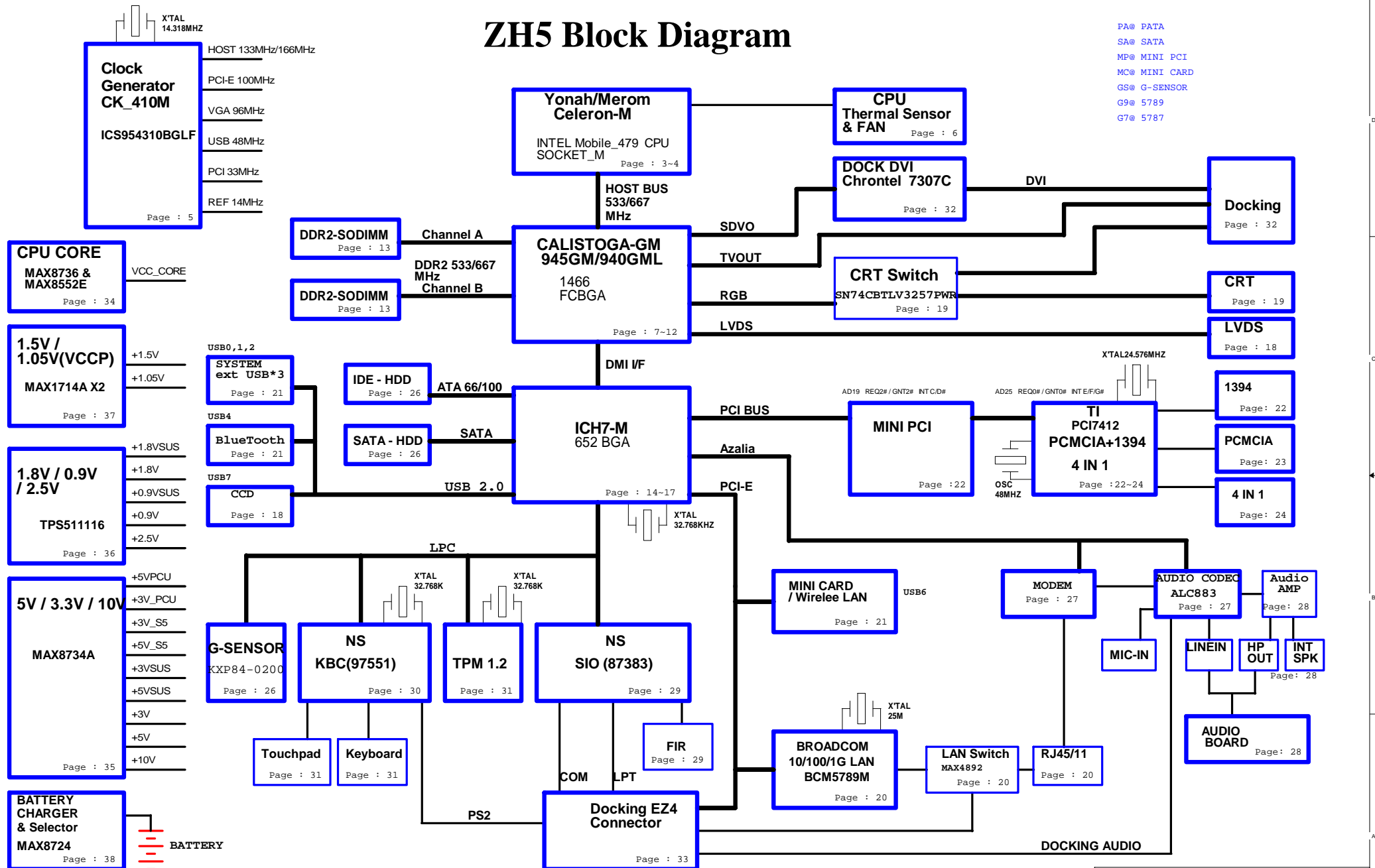


ZH5 Block Diagram

PA@ PATA
SA@ SATA
MP@ MINI PCI
MC@ MINI CARD
GS@ G-SENSOR
G9@ 5789
G7@ 5787



PCI DEVICE	IDSEL#	REQ# / GNT#	Interrupts
PCI7412	AD25	REQ0# / GNT0#	INT E/F/G#
MINI PCI	AD19	REQ2# / GNT2#	INT C/D#

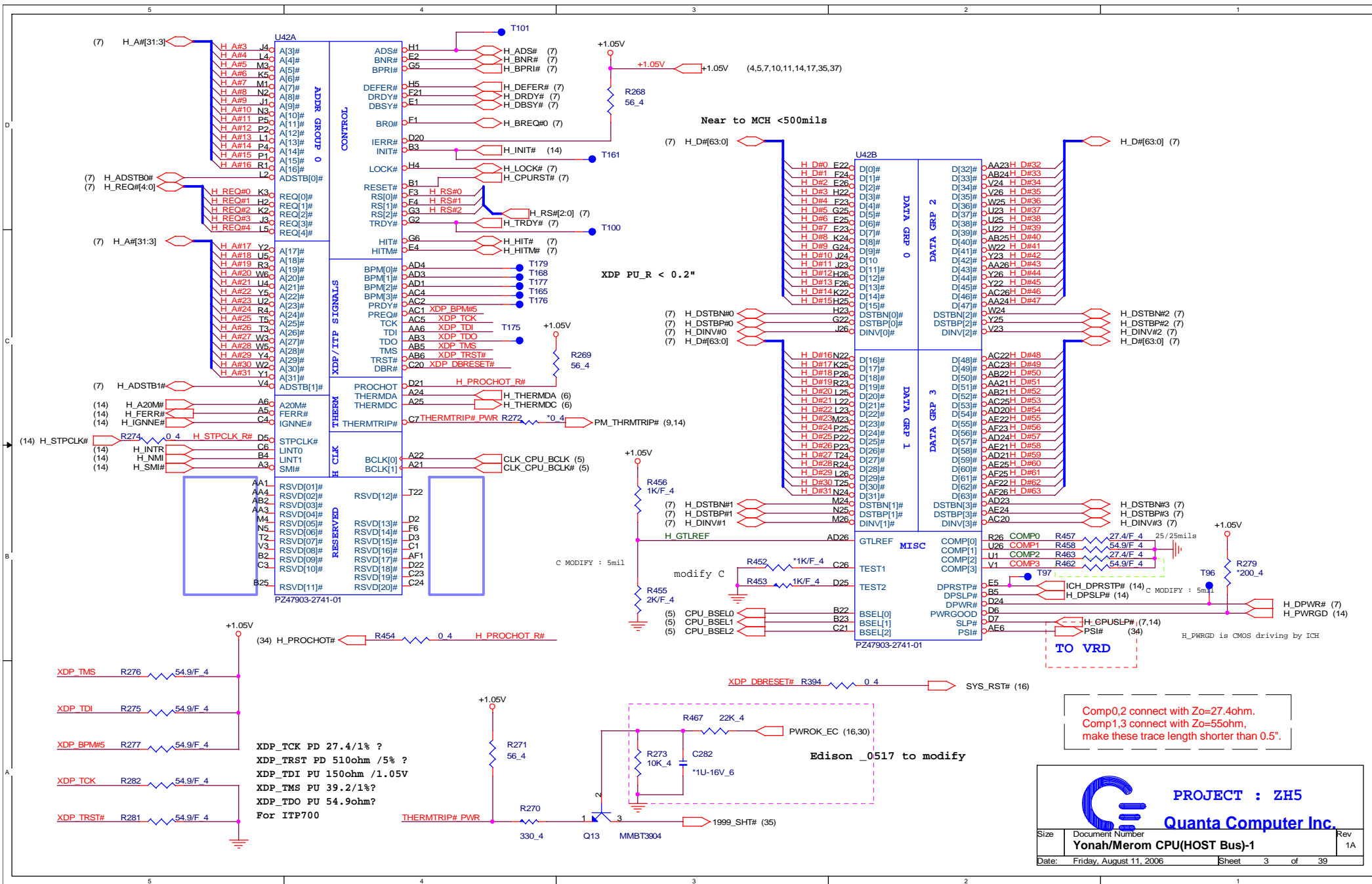
TABLE OF CONTENTS

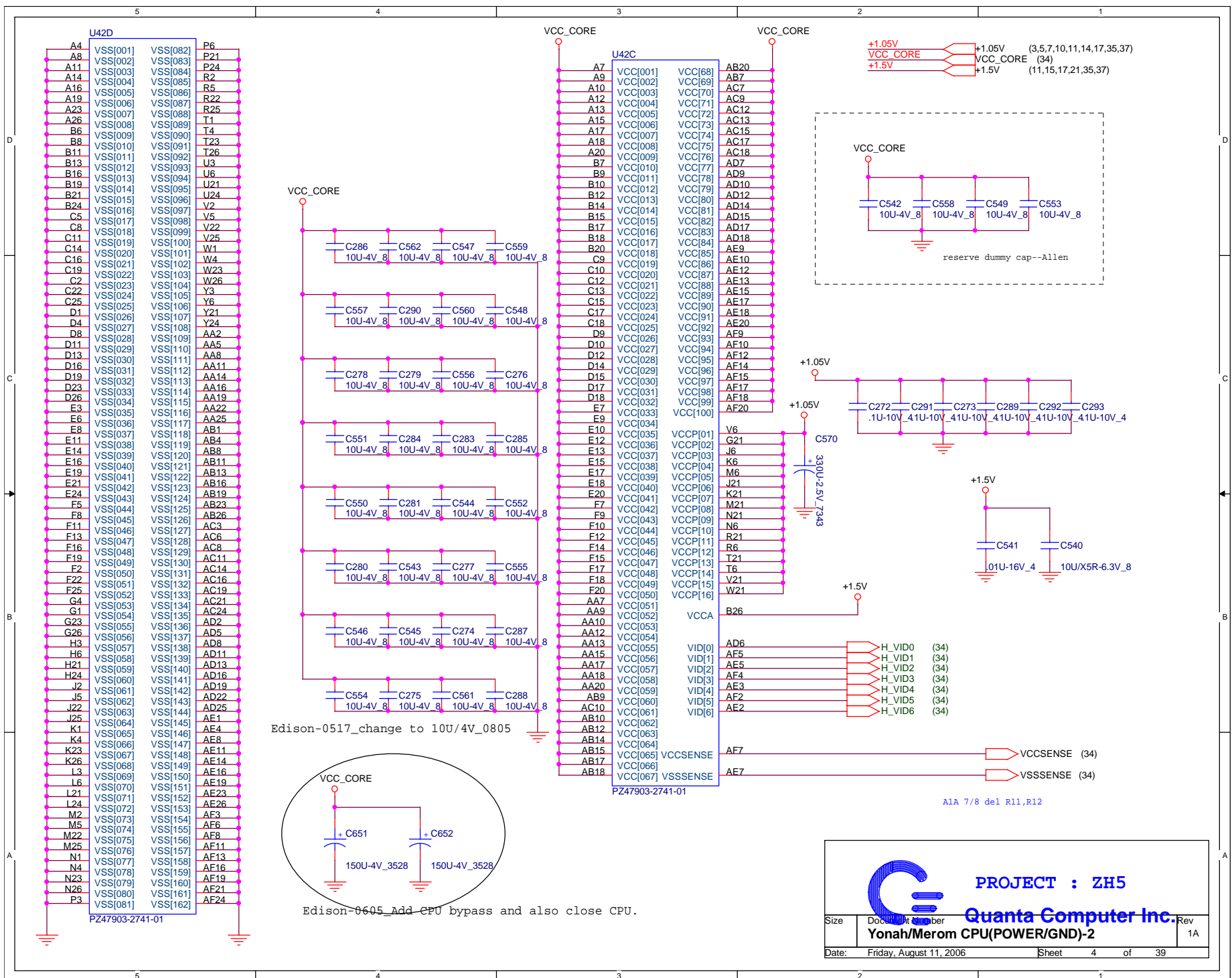
Page 01 :	BLOCK DIAGRAM
Page 02 :	TABLE OF CONTENTS
Page 03 :	Yonah/Merom CPU(HOST Bus)-1
Page 04 :	Yonah/Merom CPU(POWER/NC)-2
Page 05 :	CLOCK GENERATOR
Page 06 :	THERMAL SENSOR & FAN
Page 07 :	GMCH HOST Bus(1 of 6)
Page 08 :	GMCH DDRII MEMORY (2 of 6)
Page 09 :	GMCH DMI & VEDIO (3 of 6)
Page 10 :	GMCH POWER & STRAP(4 of 6)
Page 11 :	GMCH LVDS/DAC-PWR2 (5 of 6)
Page 12 :	GMCH GND (6 of 6)
Page 13 :	DDRII SO-DIMM & TERMINATOR
Page 14 :	ICH7-M RTC/SATA/IDE/AZALIA/CPU (1 OF 4)
Page 15 :	ICH7-M MDI/PCI E/PCI/USB (2 OF 4)
Page 16 :	ICH7-M GPIO/SMBus (3 of 4)
Page 17 :	ICH7-M POWER & GND (4 of 4)
Page 18 :	LVDS CONNECTOR & CCD
Page 19 :	CRT PORT
Page 20 :	GigaLAN BCM5787MKMLG & RJ45/RJ11
Page 21 :	PCMCIA /Mini Card /USB / Blue Tooth
Page 22 :	PCI7412-IEEE1394
Page 23 :	PCI7412-PCMCIA
Page 24 :	PCI7412-CARD READER
Page 25 :	HOLES
Page 26 :	SATA/PATA HDD /G SENSOR
Page 27 :	CODEC ALC883 & MDC
Page 28 :	AUDIO AMP & SPEAK/LIN OUT
Page 29 :	SIO (87383)
Page 30 :	EC Controller PC97551 & FLASH
Page 31 :	SWITCH,LED,KB,TP,TPM1.2,EMI PAD
Page 32 :	DOCK DVI CH7307C
Page 33 :	DOCK EZ4 CONNECTOR
Page 34 :	CPU CORE (MAX8736ETL+)
Page 35 :	SYSTEM +5V/+3V(MAX8734A)
Page 36 :	DDRII POWER 1.8VSUS / 0.9VTERM
Page 37 :	CHIPSET POWER +1.5V / VCCP(+1.05V)
Page 38 :	BATTERY CHARGER & SELECTOR



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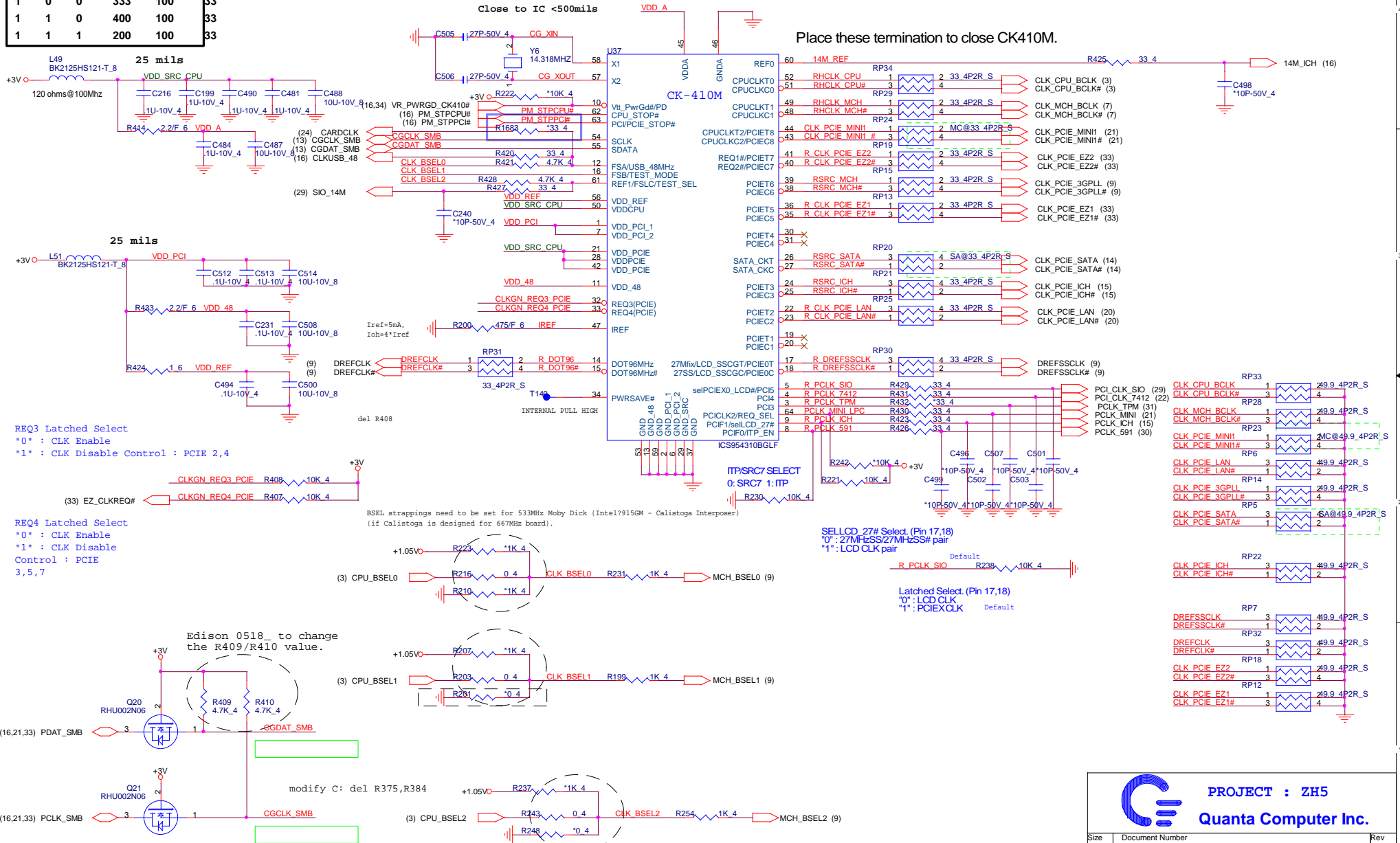
Size	Document Number	Rev
	TABLE OF CONTENTS	1A
Date:	Friday, August 11, 2006	Sheet 2 of 39

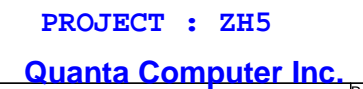
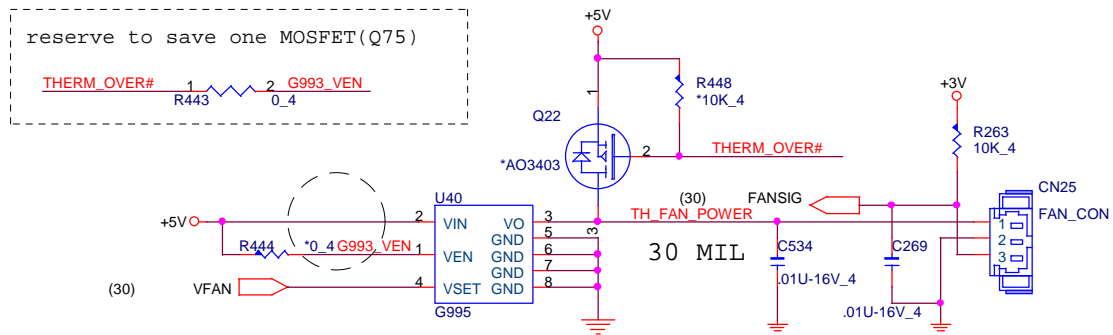




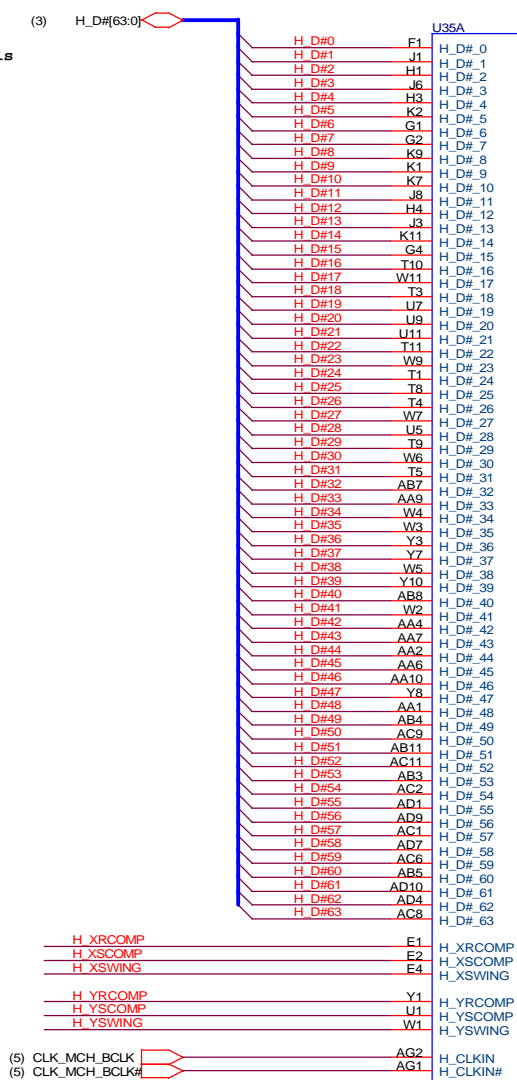
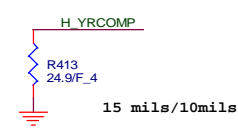
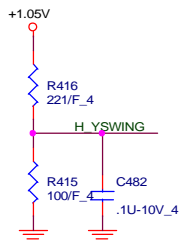
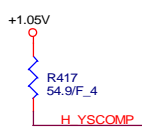
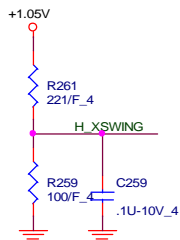
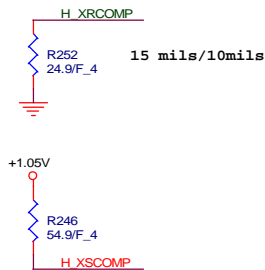
FSC	FSB	FSA	CPU	SRC	PCI
1	0	1	100	100	33
0	0	1	133	100	33
0	1	1	166	100	33
0	1	0	200	100	33
0	0	0	266	100	33
1	0	0	333	100	33
1	1	0	400	100	33
1	1	1	200	100	33

Default

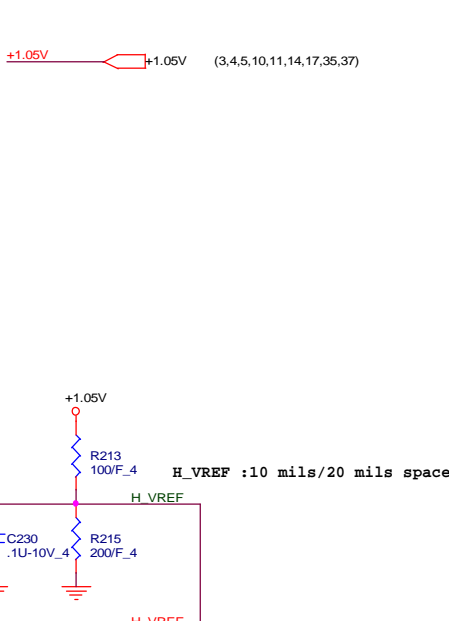
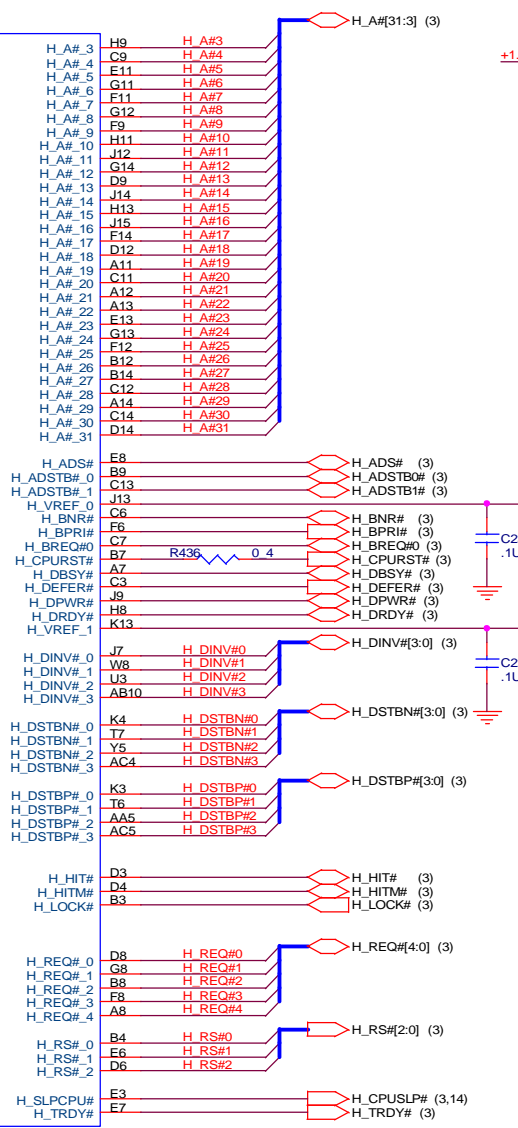




Size	Document Number CPU THERMAL SENSOR & FAN	Rev 1A
Date:	Friday, August 11, 2006	Sheet 6 of 39



HOST



COMPONENTS	P/N
945GM	AJ009450T04

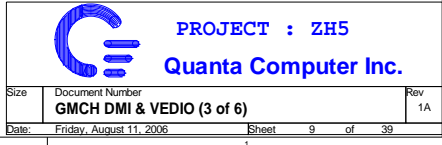
Short Stub < 100mils
extract from same point

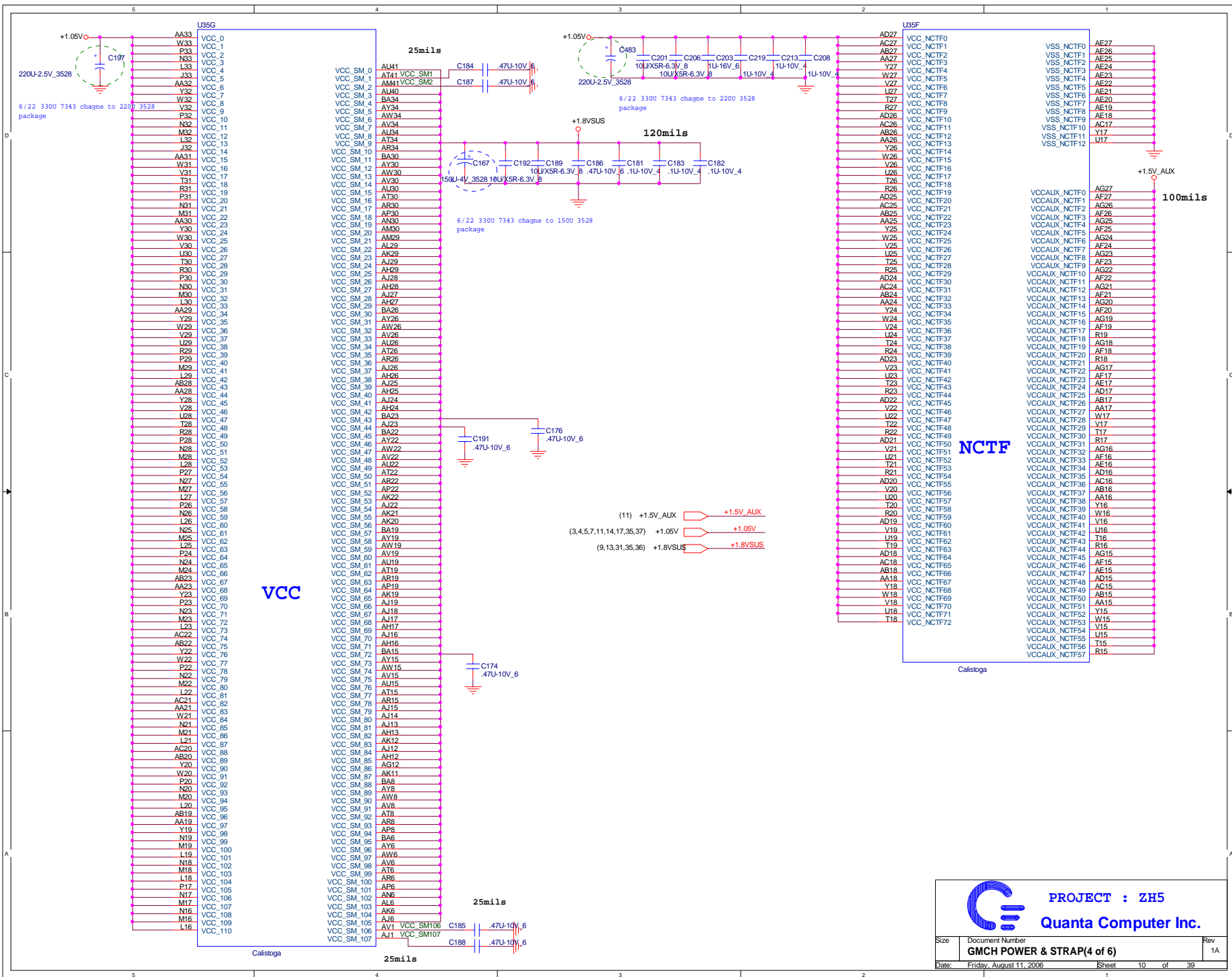
Calistoga

PROJECT : ZH5
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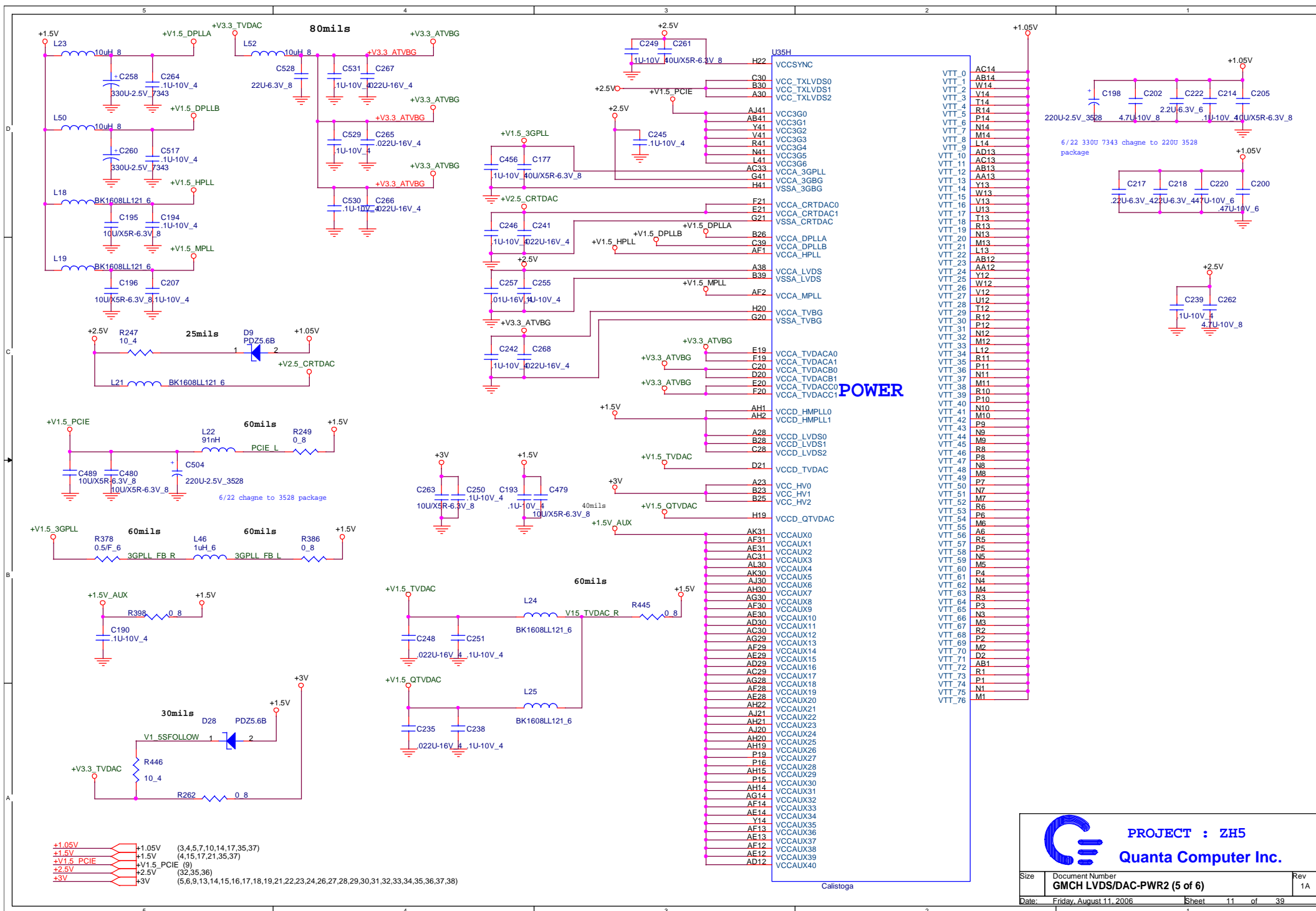
Size	Document Number	Rev
	GMCH HOST Bus(1 OF 6)	1A

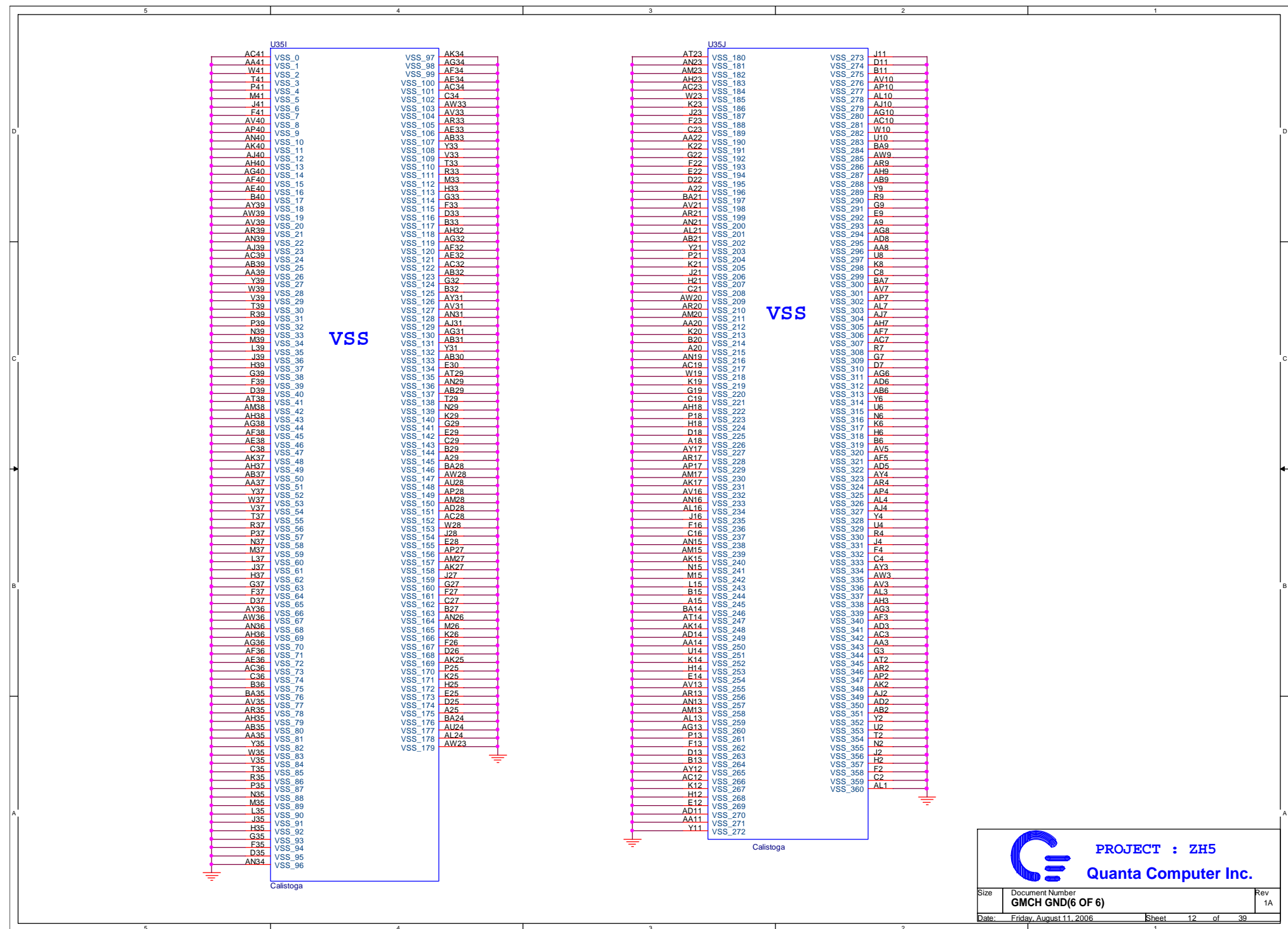
Date: Friday, August 11, 2006 Sheet 7 of 39



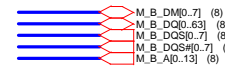
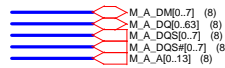
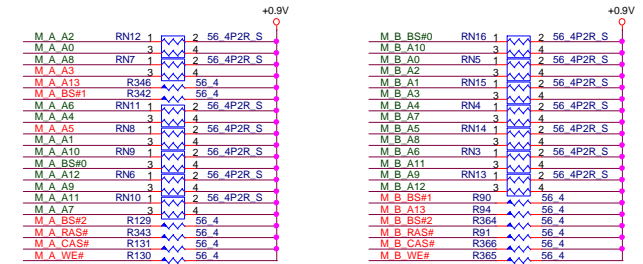
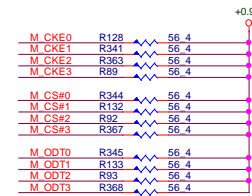
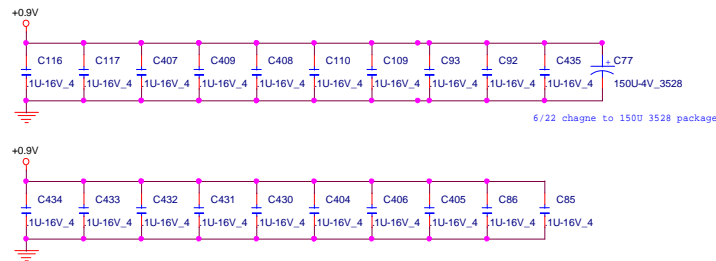


PROJECT : ZH5
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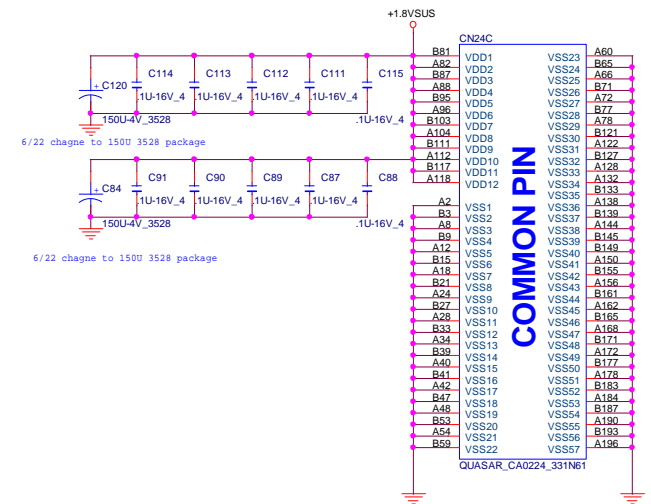
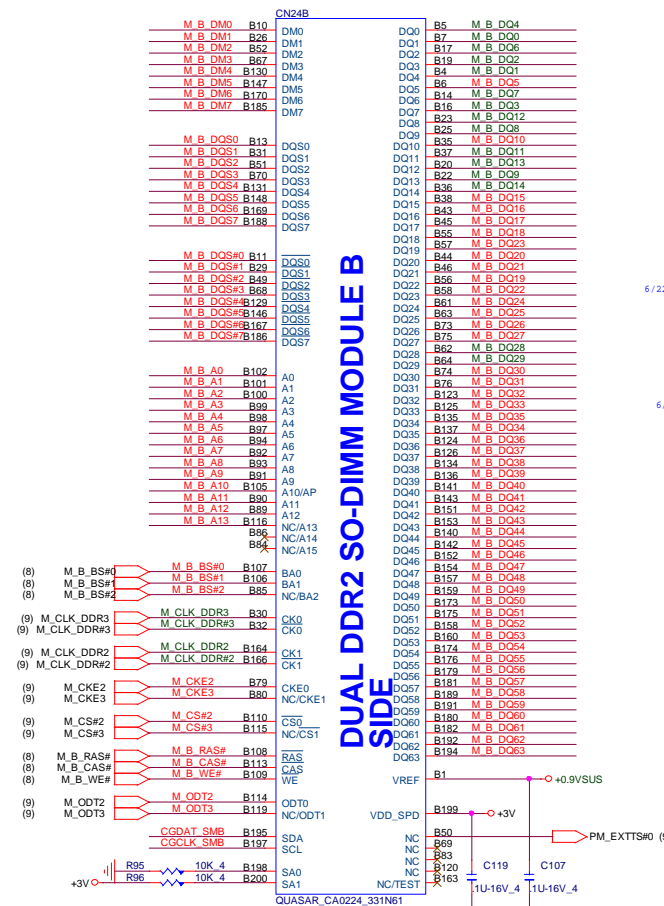
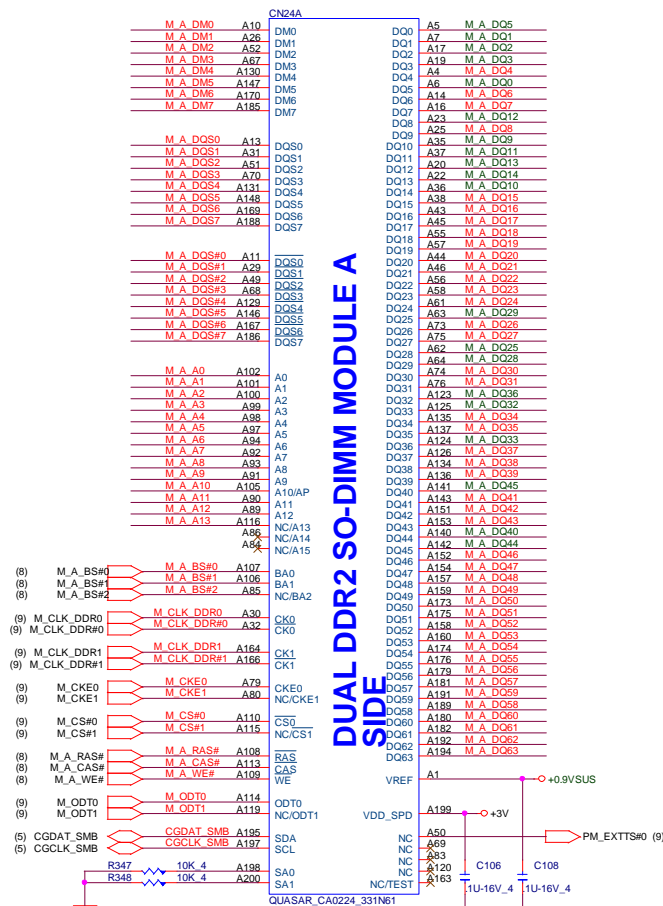




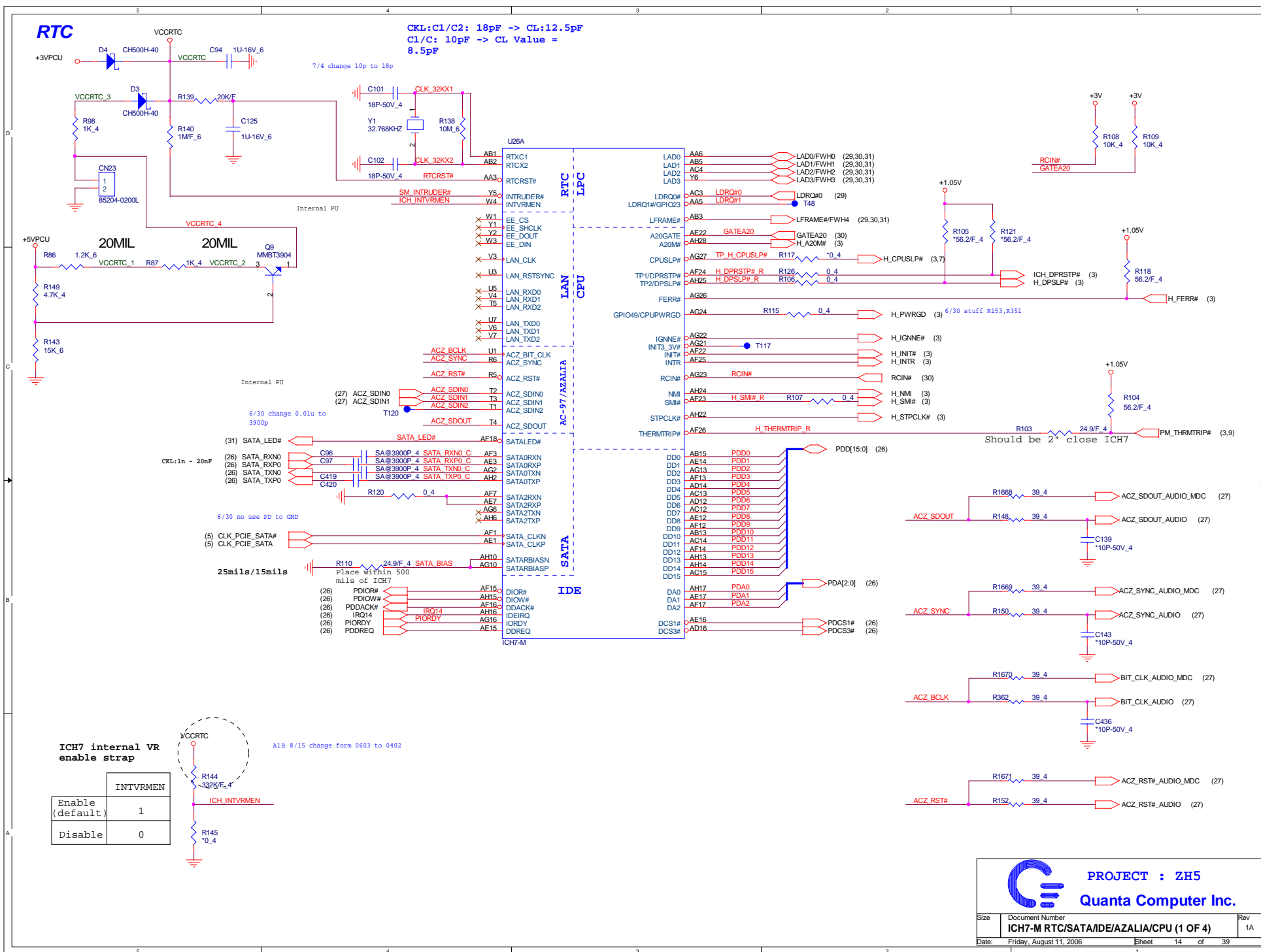
DDR2 TERMINATOR



DDR2 SO-DIMM SOCKET



COMMON PIN



modify C

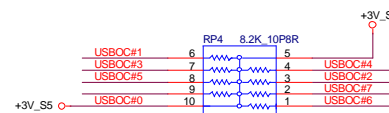
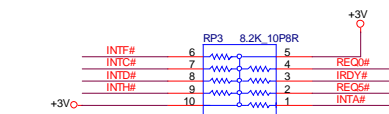
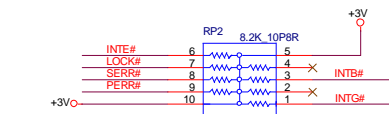
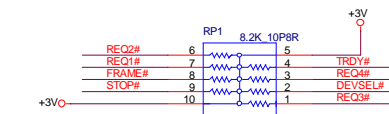
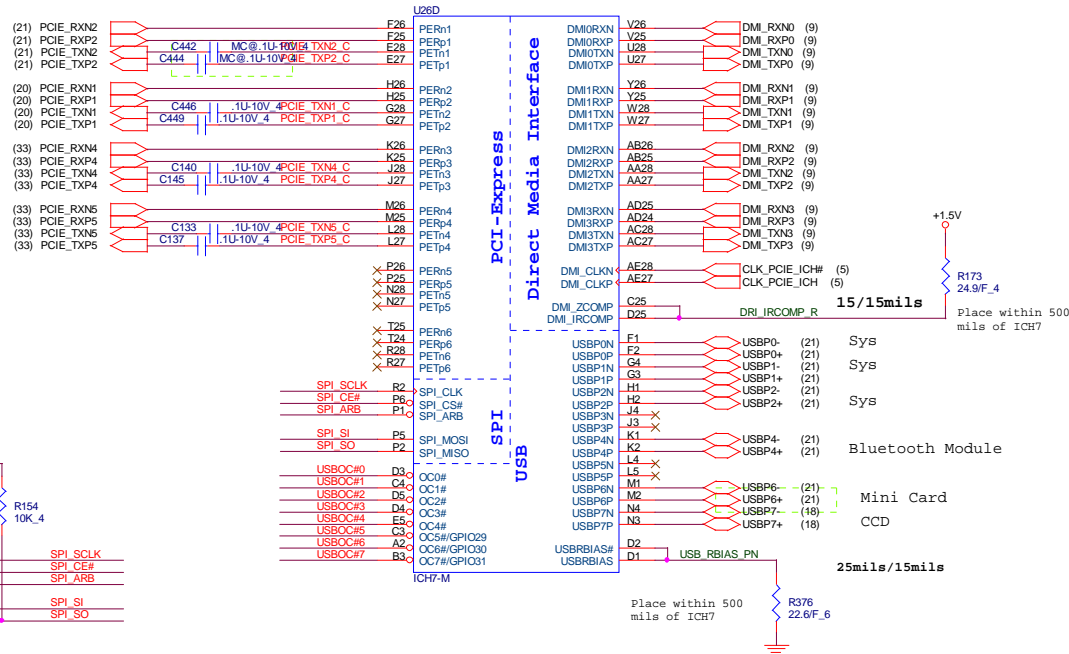
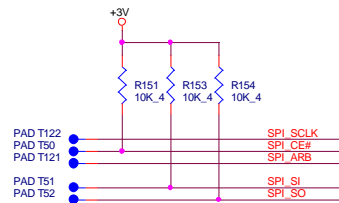
MINI CARD PCI-E

5789M

E24

E24

A1A 8/1 chagne PCI-E signal for different SB use

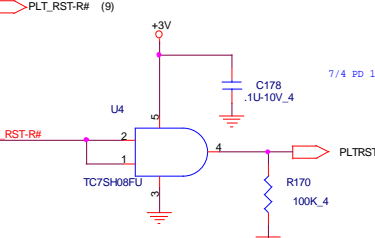


CKL use 10Kohm

ICH7 Boot BIOS select

	STRAP	GNT5# R1	GNT4# R2
LPC (default)	11	UNSTUFF	UNSTUFF
PCI	10	UNSTUFF	STUFF
SPI	01	STUFF	UNSTUFF

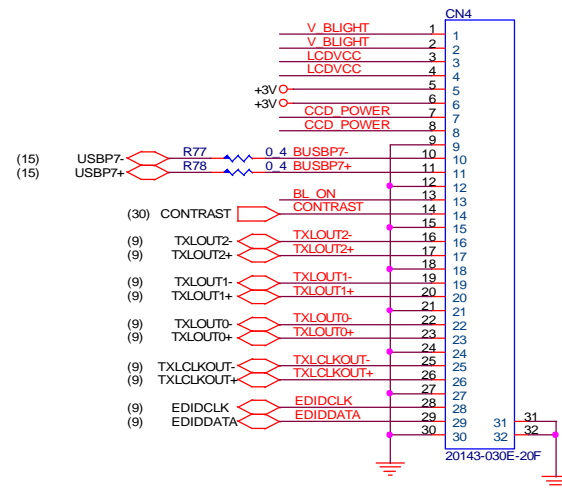
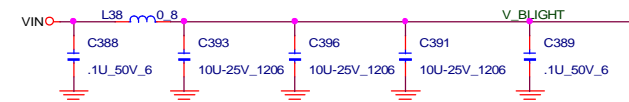
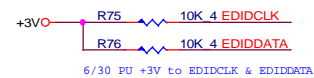
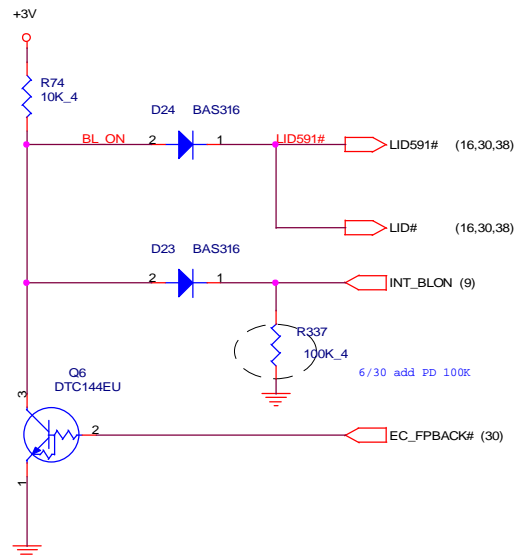
PCI DEVICE	IDSEL#	REQ# / GNT#	Interrupts
PCI7411	AD25	REQ0# / GNT0#	INT E/F/G#



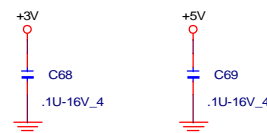
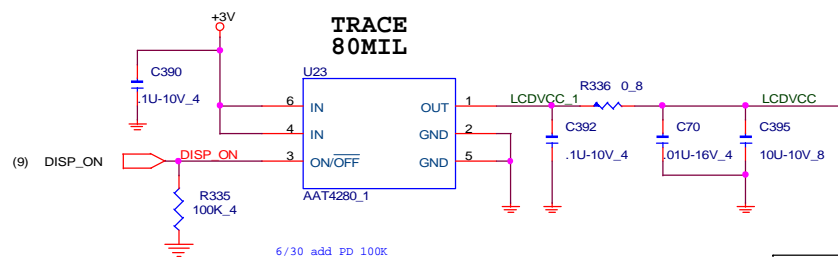
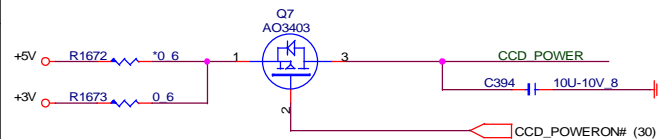
Don't connect to PCI device / Express card




PROJECT : ZH5
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CAMERA MODULE CONNECTOR





PROJECT : ZH5
Quanta Computer Inc.

Size	Document Number	Rev
	LVDS CONNECTOR & CCD	1A
Date:	Friday, August 11, 2006	Sheet 18 of 39

(9) VGA_RED
(9) VGA_GRN
(9) VGA_BLU

(9) VSYNC
(9) HSYNC

(9) CRTDCLK
(9) CRTDDAT

(33) PR_INSERT_5V

VGA RED
VGA GRN
VGA BLU

U6

VCC
A0
A1
B0
B1
C0
C1
C2
D0
D1
SE
EN#
GND

16
2
3
5
6
11
10
14
13
8

+5V
VGA RED SYS
VGA RED PR
VGA GRN SYS
VGA GRN PR
VGA BLU SYS
VGA BLU PR

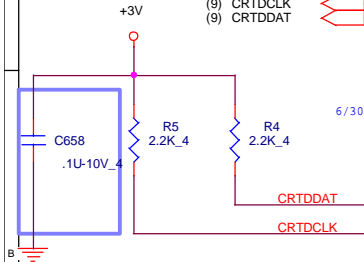
VGA_RED_PR (33)
VGA_GRN_PR (33)
VGA_BLU_PR (33)

+5V
C311
.1U-10V_4

+5V
C1
.1U-10V_4

+5V
C327
.1U-10V_4

SEL	FUNCTION
LOW	IN_B0
HIGH	IN_B1



6/30 PU +3V to CRTDCLK & CRTDDATA

Edison 0523 Delete F2.

+5V

CRTVDD2

2

D10

SSM14

1

CRTVDD3

25 MIL

(16) CRT_SENSE#

CN15

CRT_070546FR015S209CL

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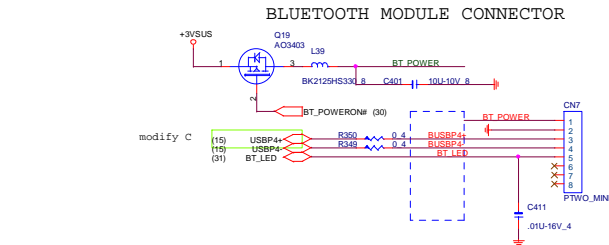
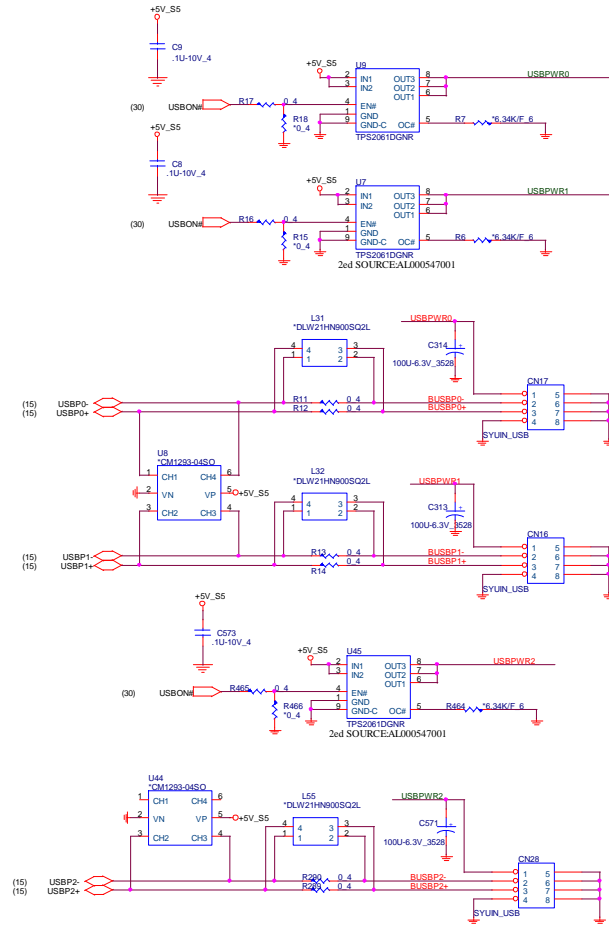
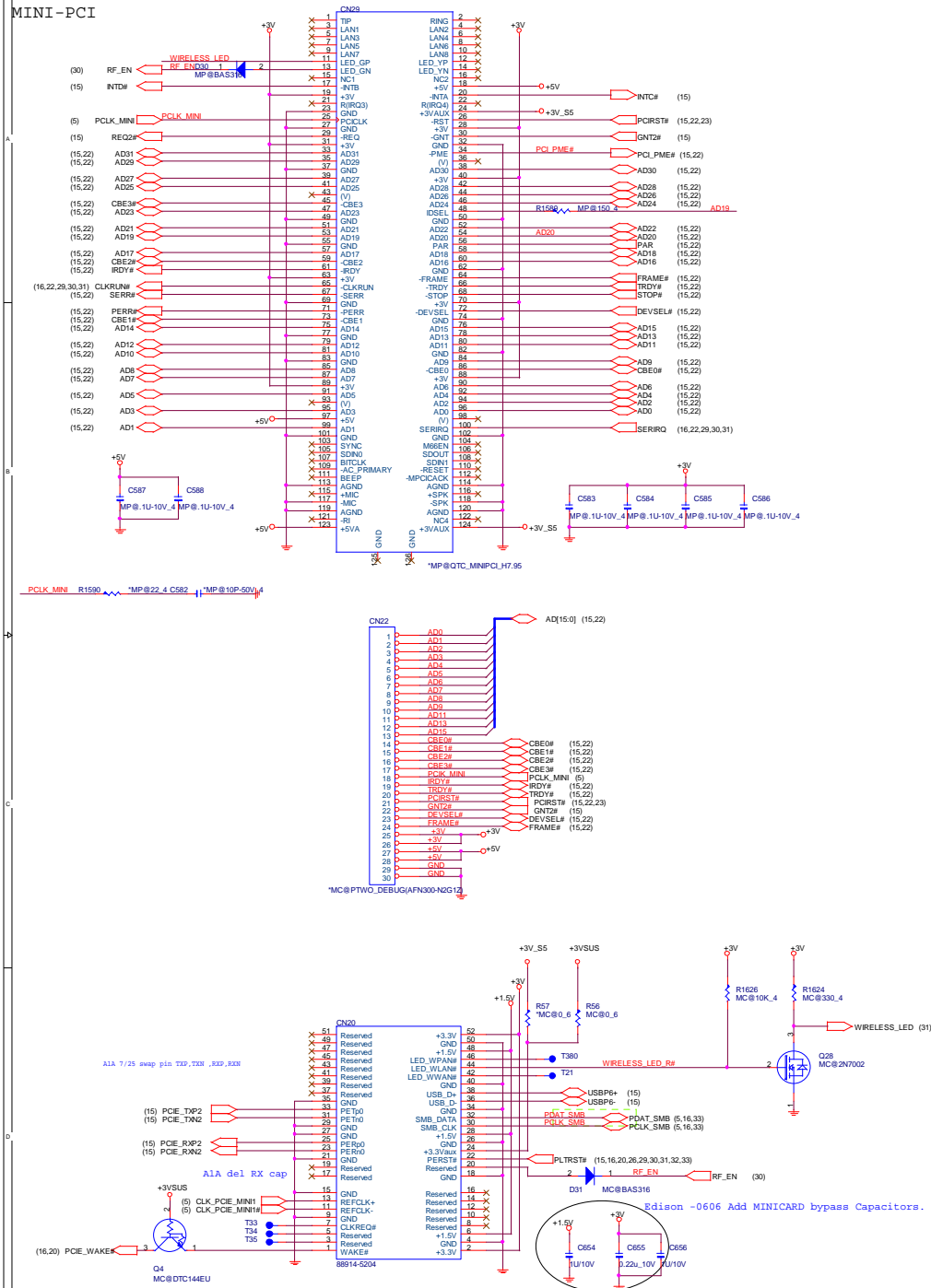
269

270

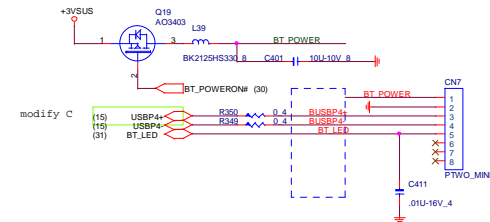
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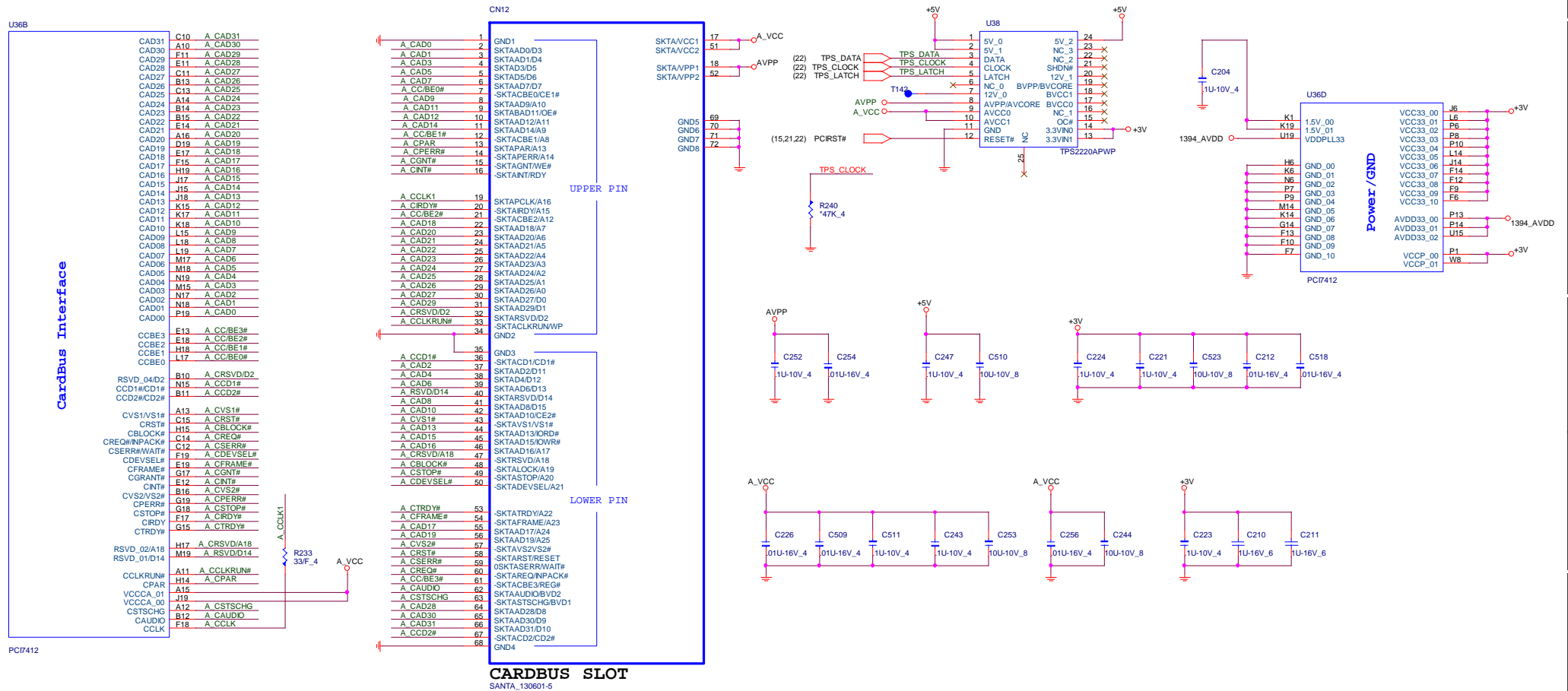
MINI-PCI



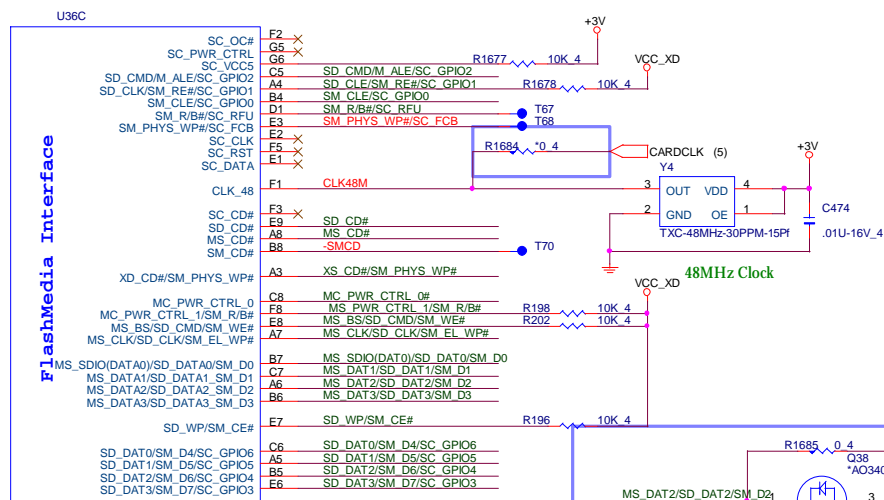
BLUETOOTH MODULE CONNECTOR



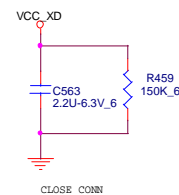
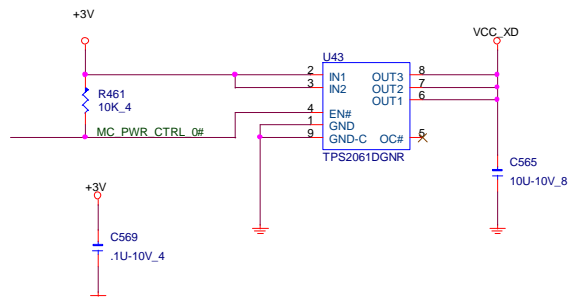
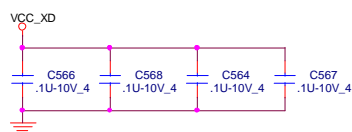
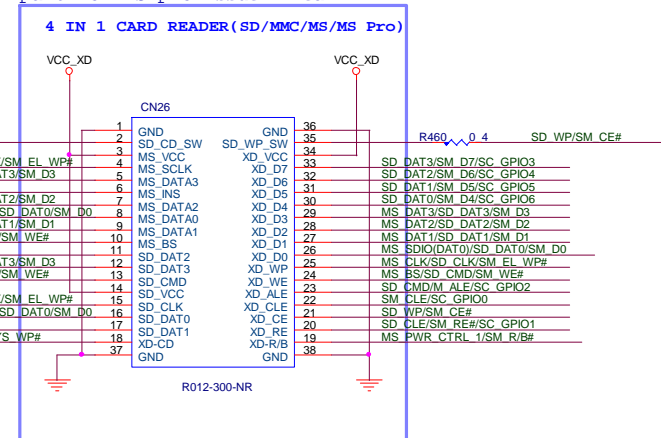




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B2A: Change CN26, modify pin define and update part for MS pro issue fixed

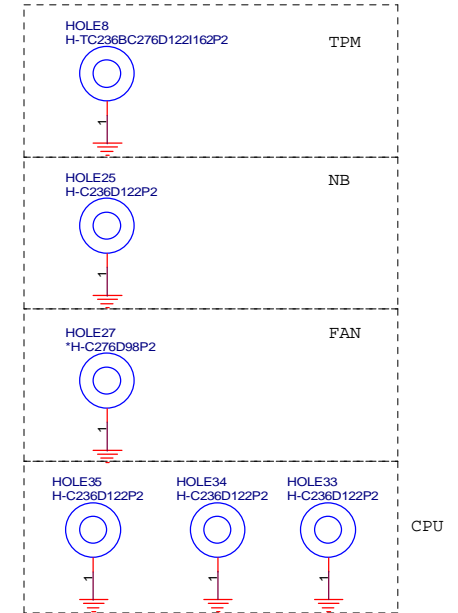
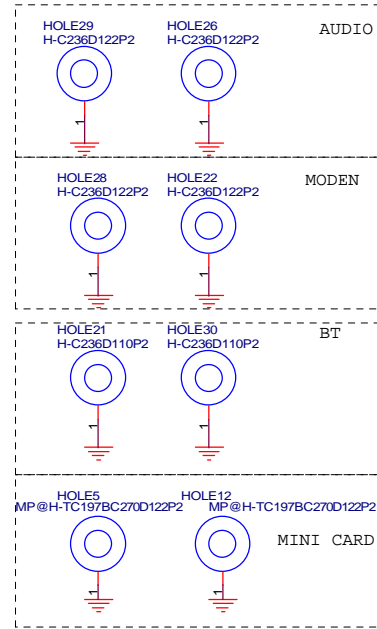
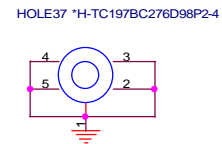
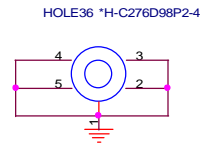
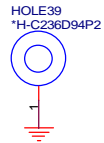
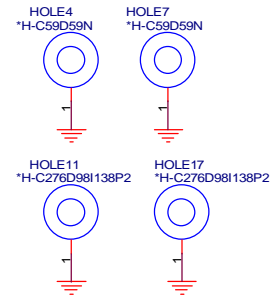
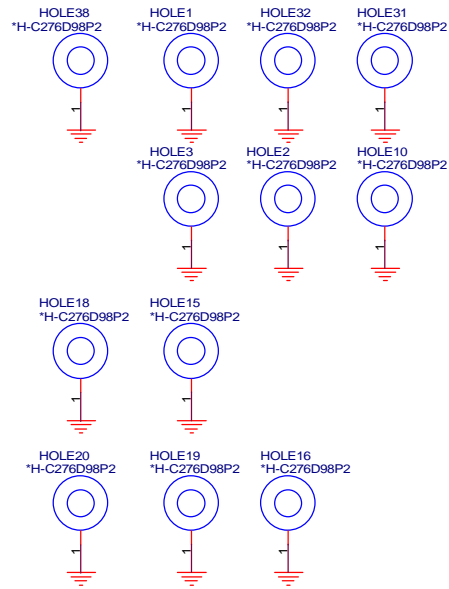


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Size	Document Number	Rev
	PCI7412-CARD READER	1A
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TOP

BOT



LINE IN



PROJECT : ZH5
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Size	Document Number CODEC ALC883 & MDC	Rev 1.
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Package 0805 for THD+N performance for Vista Logo requirements.

Speaker & Hear-Phone Amplifier

U30 MAX9789A

MAX9789A
TQFN
32PIN

For Speaker

Layout Note:
The C525 close pin18,
The C524 close pin8.

Layout Note:
The C521 close pin9,
The C451 close pin17.

Add A-LDO
option
resistor
---Edison_0628

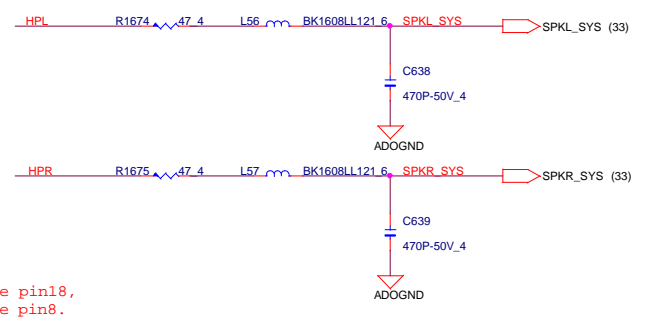
GAIN1	GAIN2	GAIN
0	0	6dB
0	1	10dB
1	0	15.6dB
1	1	21.6dB

B2A: Unmount R563, Mount R565

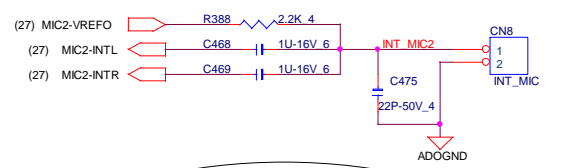
AUDIO BOARD

SPEAKER CONNECTOR

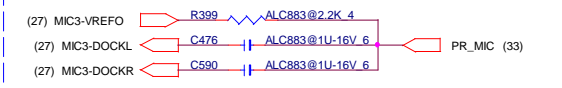
DOCK HEADPHONE



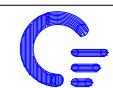
MIC



The Placement close to U27(ALC883/ALC268)
Docking Microphone Input for ALC883 ---Edison_0628



Docking Microphone Input for ALC268 ---Edison_0628



PROJECT : ZH5
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Size	Document Number	Rev
	AUDIO AMP & SPEAK/LIN OUT	1A
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SIO

